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examines all important aspects of integrated circuit design fabrication assembly and test processes as they relate to quality and reliability this second edition discusses in detail the latest circuit design technology trends the sources of error

in wafer fabrication and assembly avenues of contamination new ic packaging methods new in line process monitors and test structures and more this work should be useful to electrical and electronics quality and reliability and industrial engineers computer scientists integrated circuit manufacturers and upper level undergraduate graduate and continuing education students in these disciplines

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this book explores the synergy between very large scale integration vlsi and machine learning ml and its applications across various domains it investigates how ml techniques can enhance the design and testing of vlsi circuits improve power efficiency optimize layouts and enable novel architectures this book bridges the gap between vlsi and ml showcasing the potential of this integration in creating innovative electronic systems advancing computing capabilities and paving the way for a new era of intelligent devices and technologies additionally it covers how vlsi technologies can accelerate ml algorithms enabling more efficient and powerful data processing and inference engines it explores both hardware and software aspects covering topics like hardware accelerators custom hardware for specific ml tasks and ml driven optimization techniques for chip design and testing this book will be helpful for academicians researchers postgraduate students and those working in ml driven vlsi

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